

Abstracts

A GaAs Double-Balanced Dual-Gate FET Mixer IC for UHF Receiver Front-End Applications (Dec. 1985 [T-MTT])

K. Kanazawa, M. Kazumura, S. Nambu, G. Kano and I. Teramoto. "A GaAs Double-Balanced Dual-Gate FET Mixer IC for UHF Receiver Front-End Applications (Dec. 1985 [T-MTT])." 1985 Transactions on Microwave Theory and Techniques 33.12 (Dec. 1985 [T-MTT] (1985 Symposium Issue)): 1548-1554.

A double-balanced dual-gate FET mixer has been developed for application in the front-end circuit of UHF receivers. A 6--8-dB conversion gain has been obtained without an additional matching circuit over a wide frequency range from 100-800 MHz with good suppression of RF/LO feedthrough by more than 20 dB and third-order intermodulation product of --60 dB.

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